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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/656,504	09/07/2000	Peter Krause	004860.P2449	2231	
75	7590 04/14/2004			EXAMINER	
Andrew C Che	Andrew C Chen			YANCHUS III, PAUL B	
Blakely Sokolo	ff Taylor & Zafman LLP				
12400 Wilshire Boulevard Seventh Floor			ART UNIT	PAPER NUMBER	
			2116		
Los Angeles, CA 90025-1026			DATE MAILED: 04/14/2004	6	

Please find below and/or attached an Office communication concerning this application or proceeding.

· .	Application No.	Applicant(s)				
	09/656,504	KRAUSE ET AL.				
Office Action Summary	Examiner	Art Unit				
	Paul B Yanchus	2116				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 02 Fe	ebruary 2004.					
·	2a)⊠ This action is FINAL. 2b)□ This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) ☐ Claim(s) 1-9,11-15 and 17-22 is/are pending in 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed.  6) ☐ Claim(s) 1-9,11-15 and 17-22 is/are rejected.  7) ☐ Claim(s) is/are objected to.  8) ☐ Claim(s) are subject to restriction and/o	vn from consideration.					
Application Papers						
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomplicated any not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Examine	epted or b) objected to by the Education of the Education of by the Education of the drawing of	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date  S Patent and Trademark Office.						

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### DETAILED ACTION

This final office action is in response to amendments filed on 2/2/04.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-3, 7, 9 are rejected under 35 U.S.C. 102(b) as being anticipated by Jansen, US Patent no. 5,835,360.

Regarding claims 1 and 7, Jansen teaches a power supply circuit for a digital processing system comprising:

a first stage [31 in Figure 3] having a first output coupled to a first component of the system [Load of circuit 31 in Figure 3] and a second output which is different from the first output [Figure 3 and column 4, lines 1-5];

a second stage [32 in Figure 3] associated with a second component [Load in circuit 32 of Figure 3] of the system, said second stage coupled to said first stage [Figure 3 and column 4, lines 1-5]; and

wherein said first stage drives said second stage using the second output, and wherein the second stage transforms the second output [Figure 3 and column 4, lines 1-13].

Regarding claim 2, Jansen teaches that the first stage is separated from the second stage [Figure 3].

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Regarding claim 3, Jansen teaches that the two stages are coupled by a two wire bus [Figure 3 and column 3, lines 45-67].

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 4, 8, 11, 14 and 21-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Jansen, US Patent no. 5,835,360.

Regarding claim 4, Jansen does not explicitly show a differentially driven two-wire bus connecting the first and second stages. However, a differentially driven two-wire bus arrangement is well known in the art and it would have been obvious to one of ordinary skill in the art to use a differentially driven two-wire bus to connect the first and second stages.

Regarding claims 8 and 14, Jansen teaches an embodiment in which the first stage is a forward converter and the second stage is a flyback converter [column 6, lines 24-30]. Jansen does not explicitly teach an embodiment in which the first stage is a flyback converter and the second stage is a forward converter. However, the prior art is equivalent to the claim if "the prior art element performs the identical function specified in the claim in substantially the same way, and produces substantially the same results as the corresponding element disclosed in the specification." See MPEP § 2183(A). The

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prior art and the claimed invention both teach a power supply circuit to perform the function of powering a first component using a flyback converter and powering a second component using a forward converter. Switching the locations of the flyback converter and the forward converter in the power supply circuit does not change the intended function.

Regarding claim 11, Jansen does not explicitly teach that the second circuit and the second component are disposed on a printed circuit board. However the practice of incorporating different circuits on a printed circuit board is well known in the art and it would have been obvious to one of ordinary skill in the art to dispose the second circuit and the second component on a printed circuit board.

Regarding claims 21 and 22, Jansen does not explicitly teach that the first output provides DC voltage and the second output provides AC voltage. However AC to DC and DC to AC converters are well known in the art and it would have been obvious to employ them in the system in order to supply the appropriate voltages to components.

Claims 5, 6, 12, 13, 15 and 17-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Jansen, US Patent no. 5,835,360, in view of Applicant's Admitted Prior Art [AAPA].

Regarding claims 5, 6, 12 and 13, Jansen, as described above, teaches a power supply circuit comprising a flyback converter and a forward converter. Jansen does not explicitly teach that the first powered component is a display device and that the second powered component is a microprocessor. However, according to the AAPA, it is well known in the art that flyback converters are commonly used to supply power to display

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devices and forward converters are commonly used to supply power to CPU [AAPA, page 2, lines 1-21]. Therefore it would have been obvious to one of ordinary skill in the art that the power supply circuit taught by Jansen could be used to power a display device and a microprocessor.

Regarding claims 15 and 17-20, Jansen and the AAPA, as described above, teach a power supply circuit for a digital processing system. Therefore, Jansen and the AAPA also a system which uses the power supply circuit.

# Response to Arguments

Applicant's arguments with respect to claims 1-9, 11-15 and 17-22 have been considered but are most in view of the new ground(s) of rejection.

#### Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul B Yanchus whose telephone number is (703) 305-8022. The examiner can normally be reached on Mon-Thurs 8:00-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas Lee can be reached on (703) 305-9717. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paul Yanchus April 8, 2004

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100